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		ABALI, B., et al., IBM Research Report "Performance of Hardware Compressed Main Memory", RC 21799 (98138) 19 July 2000, ComputerScience/Mathematics, Research Division, Cover page plus pages 1-12, IBJ T.J. Watson Research Center, P.O. Box 218, Yorktown Heights, NY 10598.	
		ALAMELDEEN, A., "Adaptive Cache Compression for High-Performance Processors", 31 st Annual International Symposium on Computer Architecture (ISCA-31), Munich Germany, June 19-23, 2004, pages 1-12.	
		ABALI, B., et al., "Performance of Hardware Compressed Main Memory", 9 pages, IBM T.J. Watson Research Center, P.O. Box 218, Yorktown Heights, NY 10598.	
		ABALI, B., et al., "Memory Expansion Technology (MXT): Software Support And Performance", IBM J. Res. & Dev., Vol. 45, No. 2, March 2001, pages 287-301.	
		FRANASZEK, P.A., et al., "Algorithms And Data Structures Fro Compressed-Memory Machines", IBM J. Res. & Dev., Vol. 45, No. 2, March 2001, pages 245-258.	
		ABALI, B., et al., "Hardware Compressed Main Memory: Operating system Support and Performance Evaluation", IEEE Transactions on Computers, Vol. 50, No. 11, November 2001, pages 1219-1229	
		HALLNOR, E., et al., "A Compressed Memory Hierarchy Using An Indirect Index Cache", 2004, Cover Sheet plus pages 2-17, Advanced Computer Architecture Laboratory, EECS Department, University of Michigan, Ann Arbor, MI 48109-2122.	

Examiner Signature	/Hashem Farrokh/	Date Considered	06/27/2008
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